**PNP/NPN Epitaxial Planar Silicon Transistors** 



2SA1523/2SC3917

# Switching Applications (with Bias Resistance)

### **Applications**

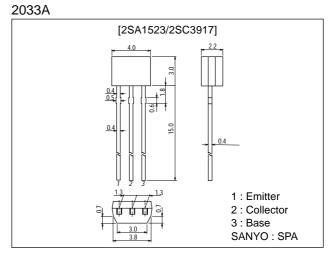
• Switching circuits, inverter circuits, interface circuits, driver circuits.

### **Features**

- · On-chip bias resistance : R1=4.7k $\Omega$ , R2=4.7k $\Omega$ .
- · Small-sized package : SPA.
- $\cdot$  Large current capacity : I<sub>C</sub>=500mA.

### Package Dimensions

unit:mm



():2SA1523

## **Specifications**

#### Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		(–)50	V
Collector-to-Emitter Voltage	VCEO		(–)50	V
Emitter-to-Base Voltage	VEBO		()6	V
Collector Current	IC		(–)500	mA
Collector Current (Pulse)	ICP		(–)800	mA
Collector Dissipation	PC		300	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

### **Electrical Characteristics** at $Ta = 25^{\circ}C$

Symbol	Conditions	Ratings			Unit
		min	typ	max	Unit
ICBO	$V_{CB}=(-)40V, I_{E}=0$			(–)0.1	μA
ICEO	V <sub>CE</sub> =(-)40V, I <sub>B</sub> =0			(–)0.5	μA
IEBO	V <sub>EB</sub> =(-)5V, I <sub>C</sub> =0	(–)410	(–)532	(–)760	μA
h <sub>FE</sub>	V <sub>CE</sub> =(-)5V, I <sub>C</sub> =(-)20mA	50			
fT	V <sub>CE</sub> =(-)10V, I <sub>C</sub> =(-)5mA		250		MHz
			(200)		MHz
	I <sub>CBO</sub> I <sub>CEO</sub> I <sub>EBO</sub> hFE	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	Symbol Conditions min typ   ICBO VCB=(-)40V, IE=0       ICEO VCE=(-)40V, IE=0  <	Symbol Conditions   ICBO VCB=(-)40V, IE=0 (-)0.1   ICBO VCE=(-)40V, IB=0 (-)0.5   IEBO VEB=(-)5V, IC=0 (-)410 (-)532   hFE VCE=(-)5V, IC=(-)20mA 50 1   fr VCE=(-)10V, IC=(-)5mA 250 1

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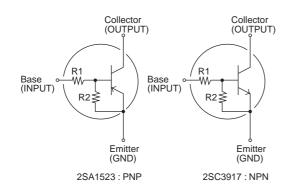
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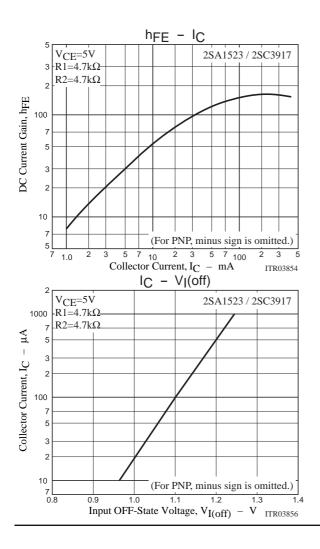
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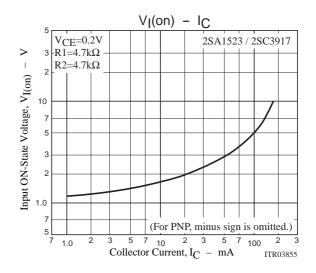
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =(-)10V, f=1MHz		3.7		pF
				(5.5)		pF
Collector-to-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =(-)40mA, I <sub>B</sub> =(-)2mA		(–)0.1	(–)0.3	V
Collector-to-Base Breakdown Voltage	V <sub>(BR)</sub> CBO	I <sub>C</sub> =(-)10μΑ, I <sub>E</sub> =0	(–)50			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I <sub>C</sub> =(−)100μA, R <sub>BE</sub> =∞	(–)50			V
Input OFF-State Voltage	V <sub>I(off)</sub>	V <sub>CE</sub> =(-)5V, I <sub>C</sub> =(-)100µA	(–)0.8	(–)1.1	(–)1.5	V
Input ON-State Voltage	V <sub>I(on)</sub>	V <sub>CE</sub> =(-)0.2V, I <sub>C</sub> =(-)20mA	(–)1.0	(–)1.9	(–)4.0	V
Input Resistance	R1		3.3	4.7	6.1	kΩ
Resistance Ratio	R1/R2		0.9	1.0	1.1	

### **Electrical Connection**







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